

FIG. 1



TITLE: ON-CHIP SUBSTRATE REGULATOR TEST MODE
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2/3

$$\begin{aligned}V_{GS} &= 1 - V_S \\V_{DS} &= -1.0 - V_S \\V_{GS} - V_{DS} &= 2 \\V_{GS} &= V_{DS} + 2\end{aligned}$$

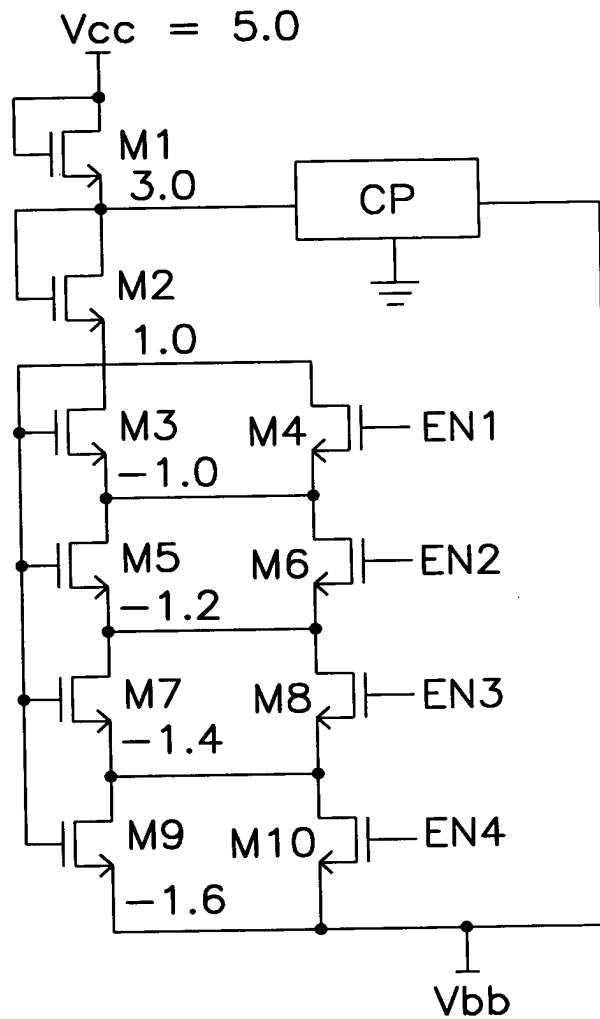


FIG. 2

FIG. 3

